

Low Profile, High Current Coupled Inductor



LINKS TO ADDITIONAL RESOURCES



FEATURES

- High temperature, up to 155 °C
- Shielded construction
- Frequency range up to 5.0 MHz
- Lowest DCR/ μ H in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- Coupling is > 90 % - optimized for SEPIC converters
- AEC-Q200 qualified
- IHCL design; PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- SEPIC converters
- DC/DC converters
- Common mode applications
- LED lighting

STANDARD ELECTRICAL SPECIFICATIONS					
	L_0 INDUCTANCE $\pm 20\%$ AT 100 kHz, 0.25 V, 0 A (μ H)	DCR NOM. 25 °C (m Ω)	DCR MAX. 25 °C (m Ω)	HEAT RATING CURRENT DC TYP. (A) ⁽¹⁾	SATURATION CURRENT DC TYP. (A) ⁽²⁾
L_{1-2}	10	90.6	97.0	4.3	8.0
L_{3-4}	10	86.0	92.0	4.2	9.0
L_{1-4} (L_{2-3} shorted)	36	176.0	189.0	2.9	4.0
L_{1-3} (L_{2-4} shorted)	1.0	176.0	189.0	2.9	See note ⁽³⁾
$L_{\text{Common Mode}}$ (1-3 and 2-4 shorted)	10	33.0	34.6	7.5	7.5
$L_{\text{Differential Mode}}$ (1-4 and 2-3 shorted)	0.0	33.0	34.6	7.5	See note ⁽³⁾

Notes

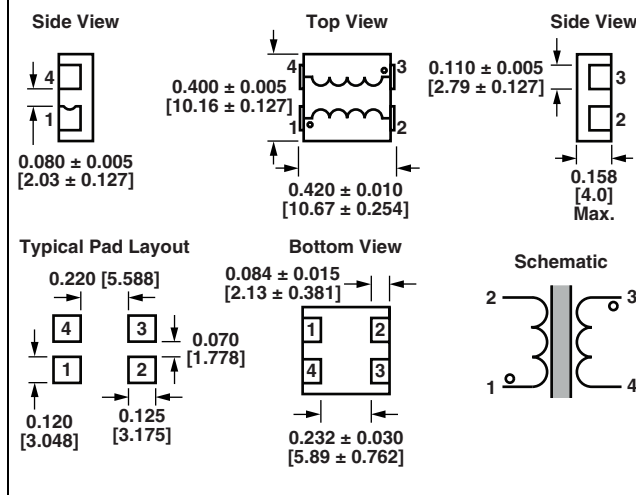
- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +155 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 50 V
- SEPIC operation can generate up to 2x the input or output voltage across the inductor. Please limit V_{IN} and V_{OUT} to 25 V max. for SEPIC operation

⁽¹⁾ DC current (A) that will cause an approximate ΔT of 40 °C

⁽²⁾ DC current (A) that will cause L_0 to drop approximately 20 %

⁽³⁾ In this configuration, current flowing opposite directions through coils cancels and the 1.0 μ H inductance is very stable with varying current. Observe the heat rating current to avoid excessive temperature rise in this configuration

DIMENSIONS in inches [millimeters]



PATENT(S): www.vishay.com/patents

This Vishay product is protected by one or more United States and international patents.



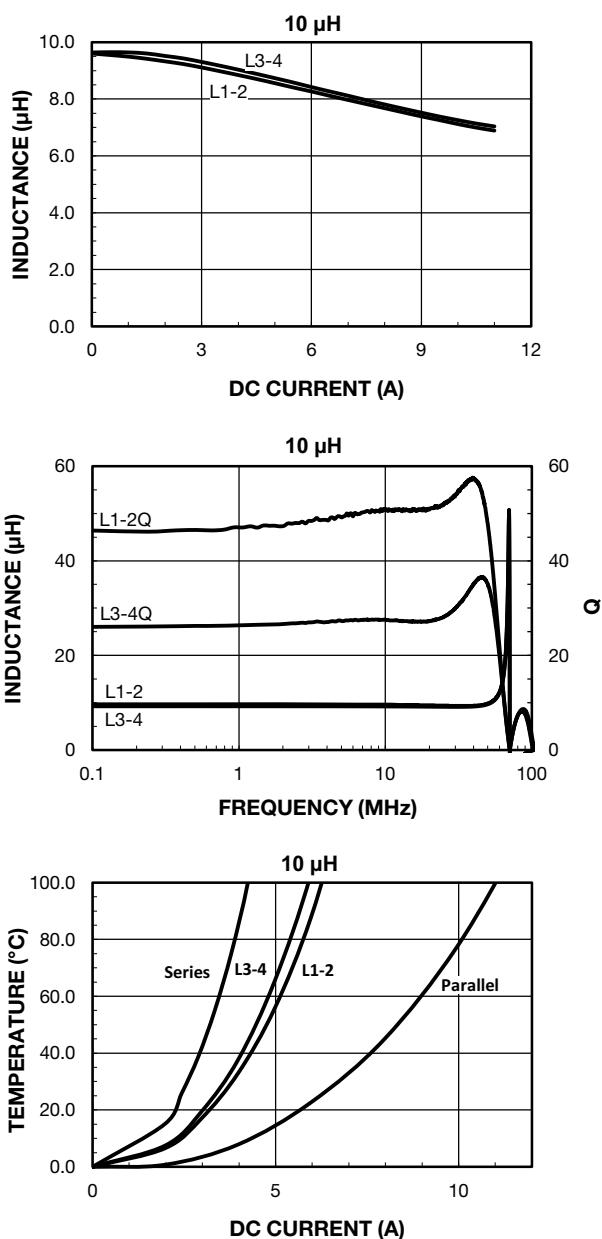
DESCRIPTION

IHCL-4040DZ-5A	10 μH	$\pm 20\%$	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

GLOBAL PART NUMBER

I	H	C	L	4	0	4	0	D	Z	E	R	1	0	0	M	5	A
PRODUCT FAMILY				SIZE				PACKAGE CODE		INDUCTANCE VALUE			TOL.	SERIES			

PERFORMANCE GRAPHS





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